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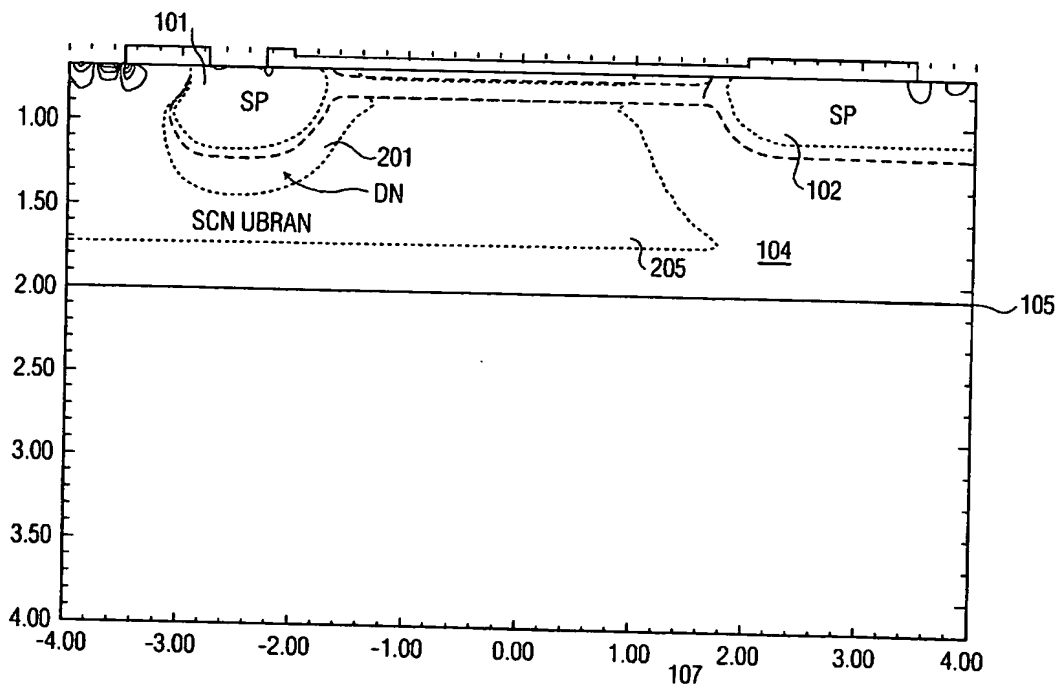
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(54) Title: PREVENTION OF PARASITIC CHANNEL IN AN INTEGRATED SOI PROCESS



(57) Abstract: A Silicon on Insulator device is disclosed wherein a parasitic channel (110) induced in a thin film portion of the device is prevented from allowing current flow between the source (101) and drain (101) by a Deep N implant directly below the source (101) and the drain (101) that would otherwise occur.

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